# **SPECIFICATIONS**

| CUSTOMER  | t :               |              |          |
|-----------|-------------------|--------------|----------|
| SAMPLE C  | ODE : <u>GFC0</u> | 802B-BNFE-JF | <u> </u> |
| DRAWING I | NO. :             |              |          |
| DATE      | :2010.01.         | .06          |          |
| CERTIFIC  | ATION:            | ROHS         |          |
| 2 ( 0:    | 0.1.0:            | A            | D        |

| Customer Sign | Sales Sign | Approved By | Prepared By |
|---------------|------------|-------------|-------------|
|               |            |             |             |
|               |            |             |             |
|               |            |             |             |
|               |            |             |             |

# **Revision Record**

| Data(y/m/d) | Ver. | Description                         | Note | page |
|-------------|------|-------------------------------------|------|------|
| 2010.01.06  | 00   | New                                 |      |      |
| 2010.08.12  | 01   | change the supplier of LED backight |      |      |
|             |      |                                     |      |      |
|             |      |                                     |      |      |
|             |      |                                     |      |      |

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#### 1. Precautions in use of LCM

- 1.1 Use Modules
  - 1. When modules switch on or off, after accessing positive supply power with  $5 \pm 0.5$  voltage ,then input signal levels, if signal levels input before supply power becomes stable or switches off, IC circuits off, modules will be damaged, as a result, modules will be damaged.
  - 2. Dot matrix modules are high path –number LCDs, they are largely related to the contrast ,view angle ,driving voltage when displaying , so you should adjust it to get best contrast and view angle, if it is too high , not only displays are effected, but also let life shorted.
  - 3. When using under regulated working temperature below, the display responsiveness it too slow, when using under regulated temperature above, whole display surface turns dark, this is not damaged, when the temperature returns normal, all displays become normal

- 1.2 Module storage
  - 1. Storaging temperature:-30~+80
  - 2. Place in dark sites to avoid strong lights
  - 3. Don't place other thing on their surfaces
  - 4. Packaged in polyer materials (with anti-static electricity layers) and sealed

1. Iron head temperature: 280±10

2. Soldering time: <3-4S

3. Soldering material: eutectic nature, low melting point

4. Don't use acid solder

5. Soldering don't repeat above 3 times

# 2. Mechanical Specifications

| Item                 | Value                          | Unit      |
|----------------------|--------------------------------|-----------|
| Number of Characters | 8X2                            | Character |
| Character Format     | 5 8 Dots                       | -         |
| Character Pitch      | 3.55(W) 5. 94(H)               | mm        |
| Character Size       | 2.96(W) X5.56(H)               | mm        |
| Dot size             | 0.56(W) 0.66(H)                | mm        |
| Dot pitch            | 0.60(W) 0.70(H)                | mm        |
| Module dimension     | 58 (W) 32(H) 13. 5NAX(T)       | mm        |
| Active Area          | 27.81(W) 11. 5(H)              | mm        |
| Viewing Area         | 38(W) X 16(H)                  | mm        |
| Lcd type             | STN BLUE Negative Transmissive |           |
| Controller           | SPLC708D1-001A                 |           |
| Duty                 | 1/16                           | -         |
| Bias                 | 1/5                            | -         |
| Viewing direction    | 6 O'clock                      | -         |
| Backlight            | White                          | -         |
| Module               | No Connector                   |           |

# 3. Backlight Characteristic

# 3.1 Electrical / optical specifications

 $Ta = 25^{\circ}C$ 

| 1a – 23 C           |                        |           |      |      |      |       |
|---------------------|------------------------|-----------|------|------|------|-------|
| Item                | Symbol                 | Condition | Min. | Тур. | Max. | Unit  |
| Forward voltage     | $V_{\mathrm{f}}$       | If=40mA,  | 3.0  | 3.2  | 3.4  | V     |
|                     |                        | White     |      |      |      |       |
| LED                 | $I_{\rm v}$            | If=40mA,  |      | 150  |      | Cd/m2 |
| *Luminous Intensity |                        | White     |      |      |      |       |
| Chromaticity        | X                      | If=40mA,  | 0.26 | 0.28 | 0.30 |       |
| Coordinate          |                        | White     |      |      |      |       |
|                     | у                      |           | 0.26 | 0.28 | 0.30 |       |
| Reverse Current     | $\mathbf{I}_{	ext{R}}$ | VR=5V,    |      |      | 0.1  | mA    |
|                     |                        | White     |      |      |      |       |

Note: \* Measured at the bare LED back-light unit.

# 3.2 LED Maximum Operating Range

| Item              | Symbol                                | WHITE | Unit |
|-------------------|---------------------------------------|-------|------|
| Power Dissipation | $P_{\scriptscriptstyle{\mathrm{AD}}}$ | 136   | mW   |
| Forward Current   | $I_{\scriptscriptstyle \mathrm{F}}$   | 40    | mA   |
| Reverse Voltage   | V <sub>R</sub>                        | 5     | V    |

### **4.Absolute Maximum Ratings**

| Item                  | Symbol   | Conditions | Min. | Max.    | Unit |
|-----------------------|--|------------|------|---------|------|
| Power supply Voltage  | Vdd  | _          | -0.3 | 7.0     | V    |
| Input voltage Range   | VIN  | _          | -0.3 | VDD+0.3 | V    |
| Operating temperature | Topr   | -          | -20  | 70      |      |
| Storage temperature   | Tstg   | -          | -30  | 80      |      |
| Static electricity    | Be sure that you are grounded when handing LCM |            |      |         |      |

**Notes:** 1. Exceeding the absolute maximum ratings may cause permanent damage to the device. Functional operation under these conditions is not implied.

### **5.DC Electrical Characteristics** (Without LED back-light)

| Characteristic                     | Symbol                           | Condition   | Min.                           | Тур. | Max.                        | Unit |
|------------------------------------|----------------------------------|---|--------------------------------|------|-----------------------------|------|
| Operating<br>Voltage               | $V_{\scriptscriptstyle DD}$      |   | 4.5                            | 5.0  | 5.5                         | V    |
| Supply<br>Current                  | $I_{\scriptscriptstyle DD}$      | Internal oscillation or external clock (V <sub>DD</sub> =5.0V,f <sub>OSC</sub> =270kHz) |                                | 1.5  | 2                           | mA   |
| Input                              | $V_{{\scriptscriptstyle IH1}}$   |   | $0.7~\mathrm{V}_{\mathrm{DD}}$ |      | $V_{\scriptscriptstyle DD}$ | V    |
| Voltage(1)<br>(except<br>OSC1)     | $V_{{\scriptscriptstyle I\!L}1}$ |   | -0.3                           |      | 0.6                         |      |
| Input                              | $V_{{	ext{IH}2}}$                |   | $V_{\rm DD}$ -1                |      | $V_{\scriptscriptstyle DD}$ | V    |
| Voltage(2)<br>(OSC1)               | $V_{{\scriptscriptstyle IL}2}$   |   |                                |      | 1.0                         |      |
| Output                             | $V_{\text{OH1}}$                 | $I_{OH}$ =-0.1mA  | 3.9                            |      | $V_{\scriptscriptstyle DD}$ | V    |
| Voltage(1)<br>(DB0 to<br>DB7)      | $V_{\scriptscriptstyle OL1}$     | I <sub>OL</sub> =0.1mA  |                                |      | 0.4                         |      |
| Output                             | $V_{\text{OH2}}$                 | I <sub>o</sub> =-40μA   | $0.9V_{\scriptscriptstyle DD}$ |      | $V_{\scriptscriptstyle DD}$ | V    |
| Voltage(2)<br>(DB0 to<br>DB7)      | $V_{ m OL2}$                     | Ιο=40μΑ   |                                |      | $0.1V_{DD}$                 |      |
| Voltage Drop                       | Vd <sub>COM</sub>                | I <sub>o</sub> =0.1mA   |                                |      | 1                           | V    |
|                                    | $Vd_{\scriptscriptstyle SEG}$    |   |                                |      | 1                           |      |
| Input<br>Leakage<br>Current        | ${ m I}_{ m IKG}$                | $V_{IN}$ =0V to $V_{DD}$  | -1                             |      | 1                           | μΑ   |
| Input Low Current                  | ${ m I}_{ m IL}$                 | $V_{IN}$ =0V, $V_{DD}$ =5V<br>(Pull Up)   | -50                            | -125 | -250                        |      |
| Internal<br>Clock<br>(external Rf) | $f_{ m OSC1}$                    | $Rf=91kO \pm 2\% (V_{DD}=5V)$   | 190                            | 270  | 350                         | kHz  |
| External                           | $f_{\rm OSC}$                    |   | 125                            | 270  | 410                         | kHz  |
| Clock                              | duty                             |   | 45                             | 50   | 55                          | %    |
|                                    | $t_{\rm R}$ , $t_{\rm F}$        |   |                                |      | 0.2                         | μS   |
| LCD Driving<br>Voltage             | $V_{LCD}$                        | V <sub>DD</sub> -V <sub>5</sub><br>(1/5,1/4 Bias)                                       | 3.0                            |      | 10.0                        | V    |

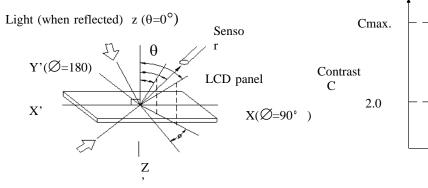
#### **6.Optical Characteristics**

1/16 duty, 1/5 bias, Vop=4.3V, Ta=25

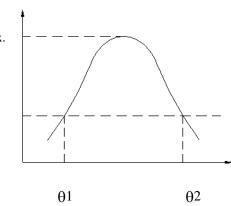
| Item                | Symbol     | Conditions  | Min. | Тур. | Max   | Reference   |
|---------------------|------------|-------------|------|------|-------|-------------|
| Driving voltage     | Vop=VDD-VO |             |      | 4.3  |       |             |
| Viewing angle       | θ          | C≥2.0,Ø=0°C | 30°  | -    |       | Notes 1 & 2 |
| Contrast            | С          | θ=5°, Ø=0°  | 3.0  | -    | -     | Note 3      |
| Response time(rise) | ton        | θ=5°, Ø=0°  | -    |      | 185ms | Note 4      |
| Response time(fall) | toff       | θ=5°, Ø=0°  | -    | -    | 167ms | Note 4      |

Note 1: Definition of angles  $\theta$  and  $\emptyset$ 

Note 2: Definition of viewing angles  $\theta 1$  and  $\emptyset 2$ 



Light (when transmitted )  $_{Y(\varnothing=0^{\circ}\ )}$  (  $\theta=90^{\circ}\ )$ 



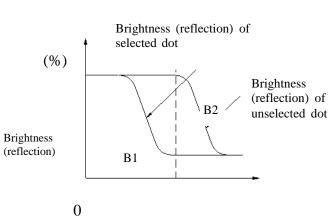
viewing angle  $\theta$  ( fixed)

Note: Optimum viewing angle with the naked eye and viewing angle  $\theta$  at Cmax. Above are not always the same

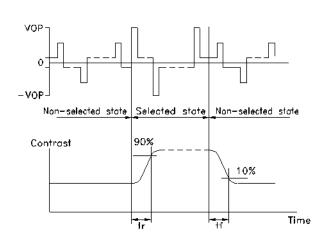
#### Note 3: Definition of contrast C

Brightness (reflection) of unselected dot (B2)

### Note 4: Definition of response time



operating voltage (v)



Note: Measured with a transmissive LCD panel which is displayed 1 cm<sup>2</sup>

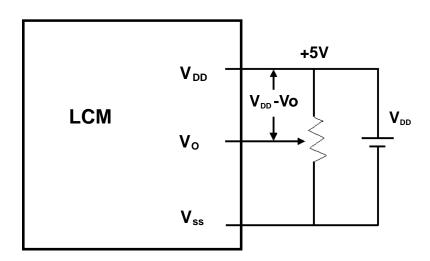
V OPR : Operating voltage  $f_{\mbox{\tiny FRM}}$  : Frame frequency

t on : Response time (rise) t <sub>OFF</sub>: Response time (fall)

# 7.Interface Pin Description

| NO.  | Symbol  | Function                         |
|------|---------|----------------------------------|
| 1    | VSS     | Ground (0V)                      |
| 2    | VDD     | Power supply for Logic circuit   |
| 3    | VO      | Power Supply for Driving the LCD |
| 4    | RS      | Data / Instruction select        |
| 5    | R/W     | Read / Write select              |
| 6    | E       | Enable signal                    |
| 7-14 | DB0-DB7 | Data Bus line                    |

# Voltage Generator Circuit



 $V_{\text{\tiny DD}}\text{-Vo}$  : LCD Driving Voltage  $V_{\text{\tiny R}}$  : 10K~20K

### 8. RELIABILITY

| Test item  | Test condition  | Evaluation and assessment   |   |
|--|---|---|---|
| Operation at high<br>temperature and<br>humidity | 40°C±2°C 90%RH for 500hours   | No abnormalities in functions* and appearance**                         |   |
| Operation at high temperature Write mode to      | $60^{\circ}\text{C}{\pm}2^{\circ}\text{C}$ for $500$ hours iming diagram (Writing Data from MPU         | No abnormalities in functions* and to SPLC78appearance**                |   |
| Heat shock                                       | -20± ~ +60°C Left for 1<br>hour at each temperature,<br>transition time 5 min,<br>repeated 10 times     | No abnormalities in functions* and tappearance**                        |   |
| Low temperature E                                | -20±2°C for 500/hours   | Vihit Vill SP2                      |   |
| Vibration  | Sweep for 1 min at 10 flz,v<br>55Hz, 10Hz, amplitude<br>1.5mm 2 hrs each in the<br>X,Y and Z directions | - to <u>functions* and</u> appearance**                                 |   |
| Drop shock RS                                    | Dropped onto a board from SPL  a height of 10cm   | No abnormalities in  Various and  Various and  Various and  Various and | _ |
| * Dissipation current,                           | contrast and display functions  | V <sub>H</sub> 1  | _ |
| ** Polarizing filter dete                        | rioration, other appearance d   | U- LHD1   |   |
| E  | VIH1<br>VIL1  | V <sub>IH1</sub><br>V <sub>IL1</sub>                                    |   |
| 3.1 Liquid crystal panel so                      | \\/\ <del>\</del>   | thD2  |   |
| 100,000 hours milin                              | num at 25 °C±10 °C VILI   | Valid Data VIH1 VIL1  |   |

# 8.2 Definition of panel service life

• Contrast/beconnel/80% Pop initialPV at SELC780D1)

|                  | rrent consumption b              | .000 1000 0000                  | Min.                  | Typ.      | Max.    | 639,85360 | ue Test Condition |
|------------------|----------------------------------|---------------------------------|-----------------------|-----------|---------|-----------|-------------------|
| • Re             | markable alignment               | deterior                        | ationocc              | curs in L | CD cell | layer     | Pin E             |
| • U <sub>1</sub> | nusual operation occ             | ars in dis                      | pla <sup>to</sup> fun | ctions    | -       | ns        | Pin E             |
|                  | E Rise/Fall Time                 | t <sub>R</sub> , t <sub>F</sub> | -                     | -         | 25      | ns        | Pin E             |
|                  | Address Setup Time               | t <sub>SP1</sub>                | 30                    | -         | -       | ns        | Pins: RS, R/W, E  |
|                  | Address Hold Time                | t <sub>HD1</sub>                | 10                    | -         |         | ns        | Pins: RS, R/W, E  |
| 0 T'             | Data Setup Time<br>aracteristics | t <sub>SP2</sub>                | 40                    | . 2       |         | ns        | Pins: DB0 - DB7   |
| 9. Timing Ch     | aracteristics                    | t                               | 10                    |           | 4111757 | ne        | Pins: DB0 - DB7   |

#### Read mode (Reading Data from SPLC780D1 to MPU)

| Characteristics        | Symbol                          |      | Limit |          | Unit  | Test Condition   |  |
|------------------------|---------------------------------|------|-------|----------|-------|------------------|--|
| Characteristics        | Syllibol                        | Min. | Тур.  | Max.     | Oilit | rest condition   |  |
| E Cycle Time           | tc                              | 400  | -     |          | ns    | Pin E            |  |
| E Pulse Width          | t <sub>w</sub>                  | 150  | -     | -        | ns    | Pin E            |  |
| E Rise/Fall Time       | t <sub>R</sub> , t <sub>F</sub> | -    | -     | 25       | ns    | Pin E            |  |
| Address Setup Time     | t <sub>SP1</sub>                | 30   | -     | <u> </u> | ns    | Pins: RS, R/W, E |  |
| Address Hold Time      | t <sub>HD1</sub>                | 10   | 2     | ~ 2×//   | ns    | Pins: RS, R/W, E |  |
| Data Output Delay Time | t <sub>D</sub>                  | -    |       | 100      | ns    | Pins: DB0 - DB7  |  |
| Data hold time PA      | AGE+9/14                        | 5.0  | 1/10  | W.       | ns    | Pin DB0 - DB7    |  |

# 10.Display Command

| Instructions |    |     |     |                | Instru | ction | Code | ;   |     | Description | Execution                     |         |
|--------------|----|-----|-----|----------------|--------|-------|------|-----|-----|-------------|-------------------------------|---------|
|              |    |     |     |                |        |       |      |     |     |             |                               | Time    |
|              | RS | R/V | VDI | 3 <b>7</b> 0B6 | DB5    | DB4   | DB3  | DB2 | DB1 | DBC         |                               | (fosc=  |
|              |    |     |     |                |        |       |      |     |     |             |                               | 270KHZ) |
| Clear        | 0  | 0   | 0   | 0              | 0      | 0     | 0    | 0   | 0   | 1           | Write "20H" to DDRAM. and set | 1.52ms  |
| Display      |    |     |     |                |        |       |      |     |     |             | DDRAM address to "00H" from   |         |
|              |    |     |     |                |        |       |      |     |     |             | AC.                           |         |
| Return       | 0  | 0   | 0   | 0              | 0      | 0     | 0    | 0   | 1   | ×           | Set DDRAM address to "00H"    | 1.52ms  |

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| Home          |   |   |    |     |     |     |     |     |     |     | from AC and return cursor to it's     |      |
|---------------|---|---|----|-----|-----|-----|-----|-----|-----|-----|---------------------------------------|------|
|               |   |   |    |     |     |     |     |     |     |     | original position if shifted.         |      |
|               |   |   |    |     |     |     |     |     |     |     | The contents of DDRAM are not         |      |
|               |   |   |    |     |     |     |     |     |     |     | changed.                              |      |
| Entry Mode    | 0 | 0 | 0  | 0   | 0   | 0   | 0   | 1   | I/D | SH  | Assign cursor moving direction        | 38µs |
| Set           |   |   |    |     |     |     |     |     |     |     | and make shift of entire display      |      |
|               |   |   |    |     |     |     |     |     |     |     | enable.                               |      |
| Display       | 0 | 0 | 0  | 0   | 0   | 0   | 1   | D   | С   | В   | Sets display (D), cursor(C), and      | 38µs |
| ON/OFF        |   |   |    |     |     |     |     |     |     |     | blinking of cursor(B) on/off          |      |
| Control       |   |   |    |     |     |     |     |     |     |     | control bit.                          |      |
| Cursor or     | 0 | 0 | 0  | 0   | 0   | 1   | S/C | R/L | ×   | ×   | Set cursor moving and display         | 38µs |
| Display Shift |   |   |    |     |     |     |     |     |     |     | shift control bit, and the direction, |      |
|               |   |   |    |     |     |     |     |     |     |     | without changing of DDRAM             |      |
|               |   |   |    |     |     |     |     |     |     |     | data.                                 |      |
| Function Set  | 0 | 0 | 0  | 0   | 1   | DL  | N   | F   | ×   | ×   | Set interface data length (DL:4 -     | 38µs |
|               |   |   |    |     |     |     |     |     |     |     | bit/8-bit), numbers of display line   |      |
|               |   |   |    |     |     |     |     |     |     |     | (N: 1-line/2-line), display font      |      |
|               |   |   |    |     |     |     |     |     |     |     | type(F:5*8 dots/5*11 dots)            |      |
| Set CGRAM     | 0 | 0 | 0  | 1   | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Set CGRAM address in address          | 38µs |
| Address       |   |   |    |     |     |     |     |     |     |     | counter.                              |      |
| Set DDRAM     | 0 | 0 | 1  | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Set DDRAM address in address          | 38µs |
| Address       |   |   |    |     |     |     |     |     |     |     | counter.                              |      |
|               |   |   |    |     |     |     |     |     |     |     |                                       |      |
|               |   |   |    |     |     |     |     |     |     |     |                                       |      |
| Read Busy     | 0 | 1 | BF | AC6 | AC5 | AC4 | AC3 | AC2 | AC1 | AC0 | Whether during internal operation     | 0µs  |
| Flag and      |   |   |    |     |     |     |     |     |     |     | or not can be known by reading        |      |
| Address       |   |   |    |     |     |     |     |     |     |     | BF. The contents of address           |      |
|               |   |   |    |     |     |     |     |     |     |     | counter can also be read.             |      |
| Write Data to | 1 | 0 | D7 | D6  | D5  | D4  | D3  | D2  | D1  | D0  | Write data into internal RAM          | 38µs |
| RAM           |   |   |    |     |     |     |     |     |     |     | (DDRAM/CGRAM).                        |      |
| Read Data     | 1 | 1 | D7 | D6  | D5  | D4  | D3  | D2  | D1  | D0  | Read data from internal RAM           | 38µs |
| from RAM      |   |   |    |     |     |     |     |     |     |     | (DDRAM/CGRAM).                        |      |

"x":don't care

 ${\bf 11.}\ Relationship\ between\ Character\ Code (DDRAM)\ and$ 

**Character Pattern(CGRAM)** 

| Ch | arac | ter ( | Code | ) (DE | RAI | M da | ita) | (  | CGR | AM. | Add | res        | 5  |    |    | CC | 3RA | M Da | ata |    |    | Pattern   |
|----|------|-------|------|-------|-----|------|------|----|-----|-----|-----|------------|----|----|----|----|-----|------|-----|----|----|-----------|
| D7 | D6   | D5    | D4   | D3    | D2  | D1   | D0   | A5 | A4  | А3  | A2  | <b>A</b> 1 | A0 | Р7 | P6 | P5 | P4  | Р3   | P2  | P1 | P0 | number    |
| 0  | 0    | 0     | 0    | ×     | 0   | 0    | 0    | 0  | 0   | 0   | 0   | 0          | 0  | ×  | ×  | ×  | Q   |      |     |    | 0  | pattern 1 |
|    |      |       |      |       |     |      |      |    |     |     | 0   | 0          | 1  |    |    |    |     | 0    | 0   | 0  |    |           |
|    |      |       |      |       |     |      |      |    |     |     | 0   | 1          | 0  |    |    |    |     | 0    | 0   | 0  |    |           |
|    |      |       |      |       |     |      |      |    |     |     | 0   | 1          | 1  |    |    |    |     |      |     |    |    |           |
|    |      |       |      |       |     |      |      |    | •   |     | 1   | 0          | 0  |    |    |    |     | 0    | 0   | 0  |    |           |
|    |      |       | ,    | •     |     |      |      |    | •   |     | 1   | 0          | 1  |    | •  |    |     | 0    | 0   | 0  |    |           |
|    |      |       |      |       |     |      |      |    |     |     | 1   | 1          | 0  |    |    |    |     | 0    | 0   | 0  |    | !         |
|    |      |       |      |       |     |      |      |    |     |     | 1   | 1          | 1  |    |    |    | 0   | 0    | 0   | 0  | 0  |           |
|    |      |       |      |       |     |      |      |    |     |     |     |            |    |    |    |    |     |      |     |    |    |           |
|    |      |       |      |       |     |      |      |    |     |     |     |            |    |    |    |    |     |      |     |    |    |           |
| •  |      |       |      |       |     |      |      |    |     |     |     |            |    |    |    |    |     |      |     |    |    |           |
|    |      |       |      | •     |     |      |      |    |     |     | •   |            |    |    |    |    |     | •    |     |    |    |           |
| 0  | 0    | 0     | 0    | ×     | 1   | 1    | 1    | 1  | 1   | 1   | 0   | 0          | 0  | ×  | ×  | ×  |     | 0    | 0   | 0  |    | pattern 8 |
|    |      |       |      |       |     |      |      |    |     |     | 0   | 0          | 1  |    |    |    |     | 0    | 0   | 0  |    |           |
|    |      |       |      |       |     |      |      |    |     |     | 0   | 1          | 0  |    |    |    |     | 0    | 0   | 0  |    |           |
|    |      |       |      |       |     |      |      |    | Ċ   |     | 0   | 1          | 1  |    | :  |    |     |      |     |    |    |           |
|    |      |       |      |       |     |      |      |    |     |     | 1   | 0          | 0  |    |    |    |     | 0    | 0   | 0  |    |           |
|    |      |       |      | •     |     |      |      |    | •   |     | 1   | 0          | 1  |    | •  |    |     | 0    | 0   | 0  |    |           |
|    |      |       |      |       |     |      |      |    |     |     | 1   | 1          | 0  |    |    |    |     | 0    | 0   | Q  |    |           |
|    |      |       |      |       |     |      |      |    |     |     | 1   | 1          | 1  |    |    |    | Ö   | 0    | 0   | 0  | 0  |           |

\* "x": dont care

### 12. Character Pattern

| Upper                   |      |      |      |      |               |      |      |      |      |      |      | - 1  |      |      |      |      |
|-------------------------|------|------|------|------|---------------|------|------|------|------|------|------|------|------|------|------|------|
| 4 bit<br>Lower<br>4 bit | LLLL | LLLH | LLHL | LLHH | LHLL          | LHLH | LHHL | LHHH | HLLL | HLLH | HLHL | HLHH | HHLL | HHLH | нннг | нннн |
| LLLL                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| LLLH                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| LLHL                    |      |      |      |      | :::: <u>*</u> |      |      |      |      |      |      |      |      |      |      |      |
| LLHH                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| LHLL                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| LHLH                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| LHHL                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| гннн                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| HLLL                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| нггн                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| нгнг                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| нгнн                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| HHLL                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| ннгн                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
|                         |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| HHHL                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |
| нннн                    |      |      |      |      |               |      |      |      |      |      |      |      |      |      |      |      |

# 13. LCM Dimension

